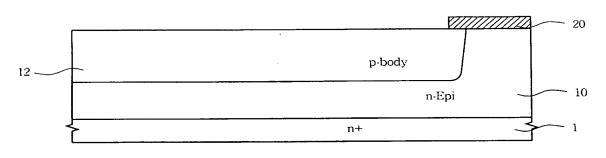
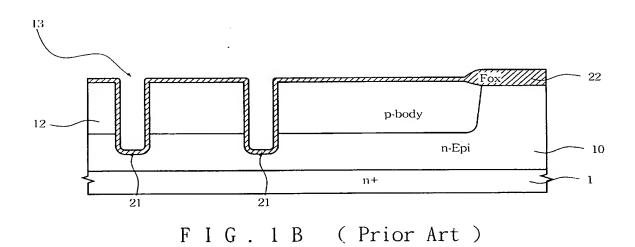
Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 1 of 17

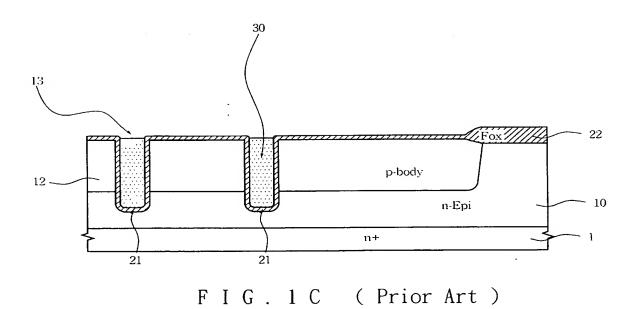


F I G . 1 A ( Prior Art )

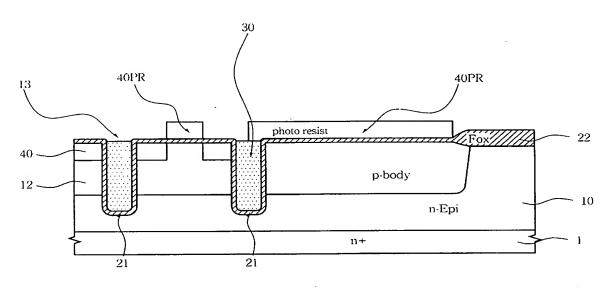
Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400
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Inventor(s): Chiao-Shun Chuang, et al.
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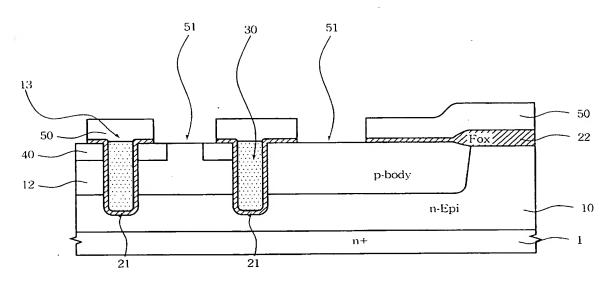


Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 4 of 17



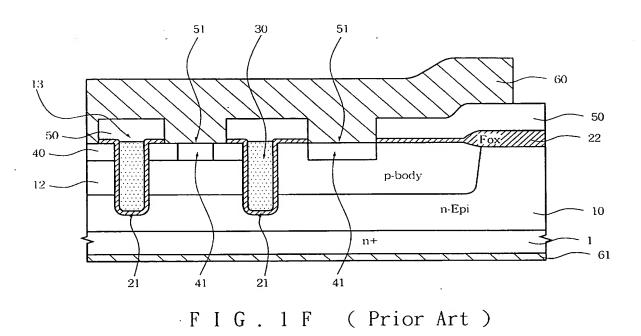
F I G . 1 D ( Prior Art )

Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 5 of 17

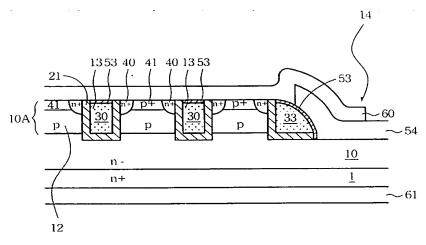


F I G . 1 E ( Prior Art )

Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 6 of 17

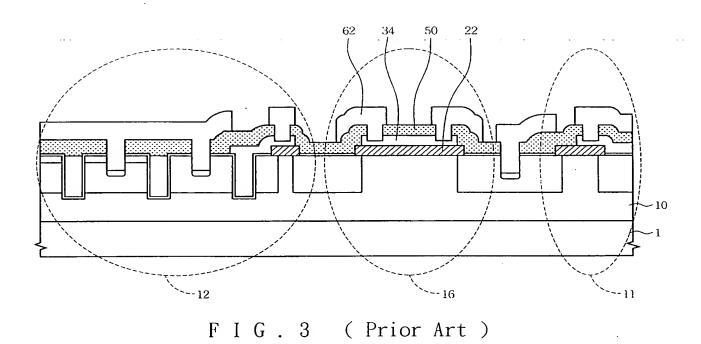


Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 7 of 17

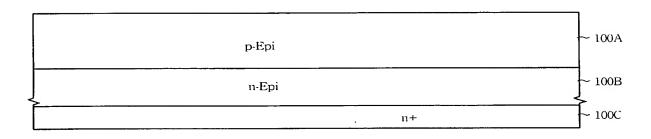


F I G . 2 (Prior Art)

Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400
Titled: Termination Structure of DMOS Device
Inventor(s): Chiao-Shun Chuang, et al.
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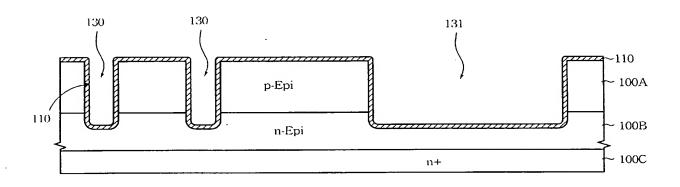


Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 9 of 17



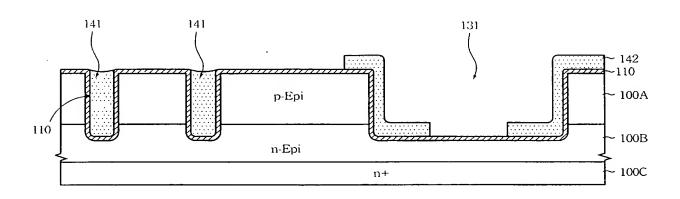
F I G . 4 A

Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 10 of 17



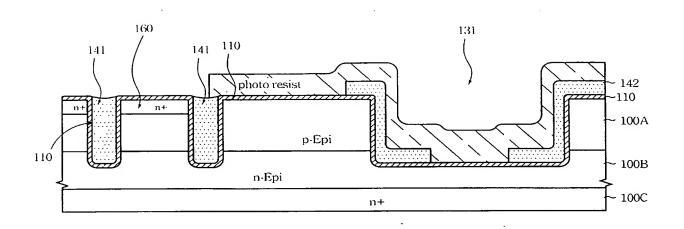
F I G . 4 B

Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 11 of 17



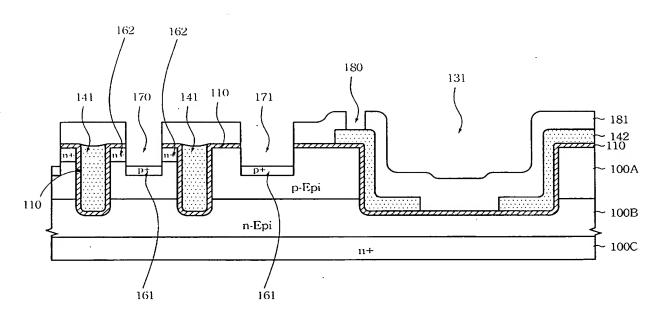
F I G . 4 C

Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 12 of 17



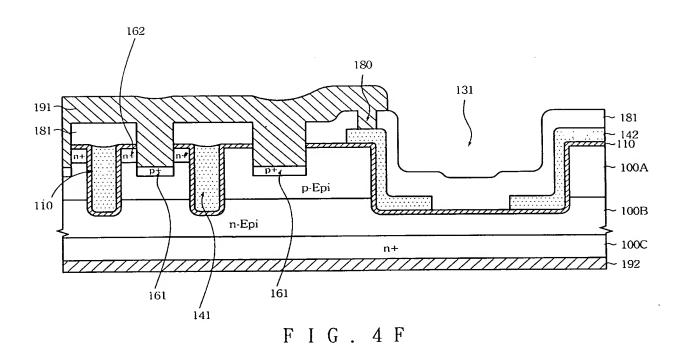
F I G . 4 D

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Titled: Termination Structure of DMOS Device
Inventor(s): Chiao-Shun Chuang, et al.
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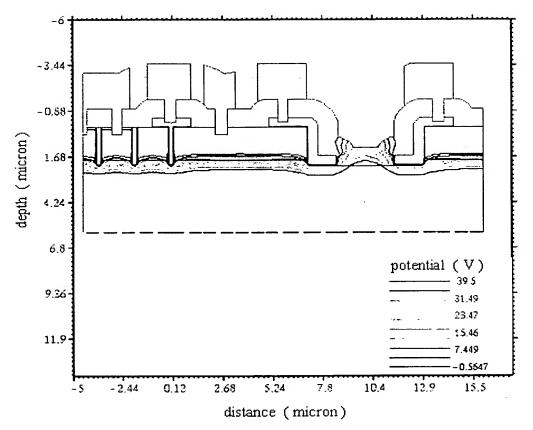
F I G . 4 E

Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 14 of 17



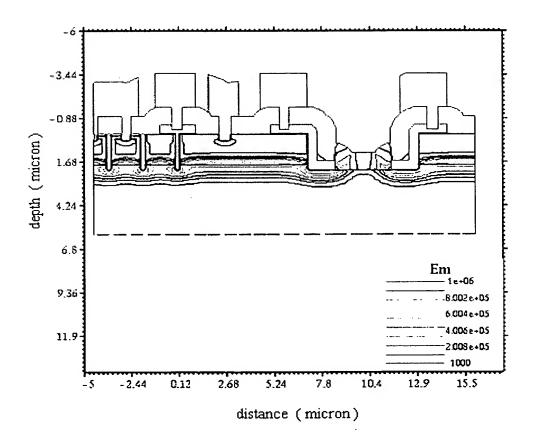
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Titled: Termination Structure of DMOS Device
Inventor(s): Chiao-Shun Chuang, et al.
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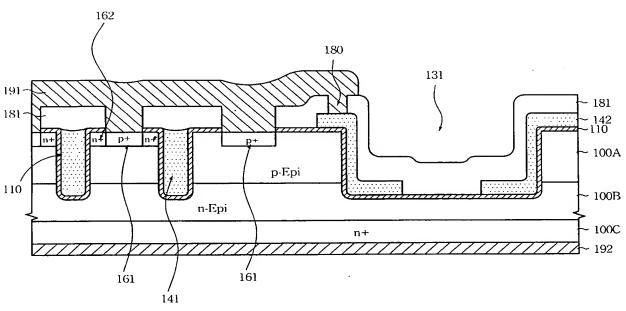
F I G . 5

Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400 Titled: Termination Structure of DMOS Device Inventor(s): Chiao-Shun Chuang, et al. Filed: February 3, 2004 Dckt. No. 17620R-002900US Sheet 16 of 17



F I G . 6

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F I G . 7